

-100mA / -50V Digital transistors

(with built-in resistors)

DTA124EM / DTA124EE / DTA124EUA / DTA124EKA

Applications

Inverter, Interface, Driver

Features

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see the equivalent circuit).
- 2)The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- Only the on / off conditions need to be set for operation, making the device design easy.

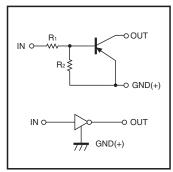
Structure

PNP epitaxial planar silicon transistor (Resistor built-in type)

Packaging specifications

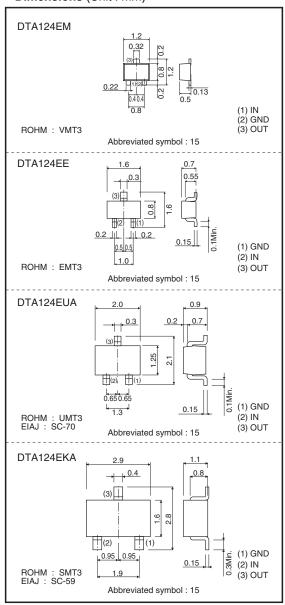
	Package	VMT3	EMT3	UMT3	SMT3
	Packaging type	Taping	Taping	Taping	Taping
	Code	T2L	TL	T106	T146
Туре	Basic ordering unit (pieces)	8000	3000	3000	3000
DTA124EM		0	-	-	-
DTA124EE		-	0	-	-
DTA124EUA		-		0	-
DTA124EKA		-	-	_	0

Inner circuit



 $R_1=R_2=22k\Omega$

• Dimensions (Unit : mm)



• Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Lin	Unit	
raiametei		DTA124EM DTA124EE	DTA124EUA DTA124EKA	Offic
Supply voltage	Vcc	!	V	
Input voltage	VIN	-40 t	V	
Output ourrent	lo	_<	mA	
Output current	IC(Max.)	-1		
Power dissipation	Po	150	200	mW
Junction temperature	Tj	15	°C	
Storage temperature	Tstg	–55 to	°C	

• Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Innuit valta aa	VI(off)	-	-	-0.5	٧	Vcc=-5V, Io=-100μA
Input voltage	VI(on)	-3	-	_		Vo=-0.2V, Io=-5mA
Output voltage	V _{O(on)}	-	-0.1	-0.3	V	lo/l≔–10mA/–0.5mA
Input current	lı	-	-	-0.36	mA	V=-5V
Output current	IO(off)	-	-	-0.5	μΑ	Vcc=-50V, Vi=0V
DC current gain	Gı	56	-	_	-	Vo=-5V, Io=-5mA
Input resistance	R ₁	15.4	22	28.6	kΩ	_
Resistance ratio	R2/R1	0.8	1	1.2	-	_
Transition frequency	f⊤ *	-	250	_	MHz	Vce=-10V, Ie=5mA, f=100MHz

^{*}Characteristics of built-in transistor.

• Electrical characteristic curves

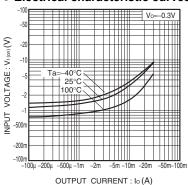


Fig.1 Input voltage vs. output current (ON characteristics)

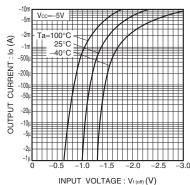


Fig.2 Output current vs. input voltage (OFF characteristics)

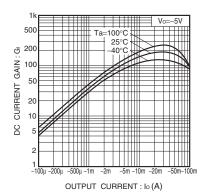


Fig.3 DC current gain vs. output current

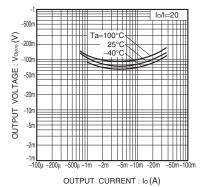


Fig.4 Output voltage vs. output current

Notes

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